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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		<i>Complete if Known</i>	
		Application Number	10/657,304
		Filing Date	9/8/04
		First Named Inventor	Kiran K. Gullapalli
		Group Art Unit	2825
Examiner Name	Lern		
Sheet	1	of	1
		Attorney Docket Number	SC12003TS

U. S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number Code ²	Kind (if known)			
NL	AA	6,031,986		Milsom	02-29-2000	
NL	AB	6,253,359		Cano et al.	06-26-2001	

FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁵
		Office Code ³	Number ⁴	Kind (if known)				
	AC	RU	2168735			10-06-2001	Abstract	
	AD	RU	93049136			10-08-1996	Abstract	

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ⁶
NL	AE	SANSEN, Willy; "Distortion in Elementary Transistor Circuits"; IEEE Transactions of Circuits and Systems-II: Analog and Digital Signal Processing; March, 1999; pp 315-325; Vol. 46, No. 3; IEEE	
NL	AF	FENG, Dan et al.; "Efficient Computation of Quasi-periodic Circuit Operating Conditions Via a Mixed Frequency/Time Approach"; Proceeding of the 36 th Design Automation Conference; June 21-25, 1999; p 635-640; IEEE	
NL	AG	CHEN, Jess et al.; "Simulation and Modeling of Intermodulation Distortion in Communication Circuits"; Proceedings of IEEE 1999 Custom Integrated Circuits Conference; May 16-19, 1999; pp 5-8; IEEE	
NL	AH	NARAYANAN, S.; "Transistor Distortion Analysis Using Volterra Series Representation"; Bell System Technical Journal; May-June, 1997; Bell Systems	
NL	AI	MAAS, Stephen A.; "Nonlinear Microwave Circuits"; 1997, pp 155-207; ISBN 0-7803-3403-5; IEEE Press	
NL	AJ	CHISHOLM, Stephen H. et al.; "Efficient Computer Simulation of Distortion in Electronic Circuits"; IEEE Transaction on Circuit Theory; November, 1973; pp742-745; IEEE	

Examiner Signature	<i>Nann Lern</i>	Date Considered	05/20/05
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Application Number	10/657,304
Filing Date	September 8, 2003
First Named Inventor	Kirun K. Guillepalli et al.
Group Art Unit	2825
Examiner Name	Unassigned
Attorney Docket Number	SC-1203T S

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Examiner Signature	<i>Name</i> <i>ben</i>	Date Considered	05/20/05
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¹ Unique citation designation number. ² See Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by its two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 is possible. ⁶ Applicant is to place a check mark here if English Language Translation is attached.